# ServerWiz2 Overview

- Serverwiz2 is a hierarchically based XML editor that is targeted for representing a system topology.
- It has 3 primary concepts:
  - Instances
    - Node, card, connector, or chip
    - Chips can have units that specify subcomponents of that chip such as cores and bus interfaces
  - Busses/Connections
    - A connection between 2 units of Instances
    - Connections are made at the level in the hierarchy where they exist in the real system
  - Attributes
    - Instances and Connections both have attributes
    - Attributes are variables that Hostboot reads to control the behavior

#### **Instance Creation**



#### **Connection Creation**

Select Bus: NONE Select Card: sys-0	nly busses of selected type	<ol> <li>Select a bus type from dropdown</li> <li>Select the card on which the bus is on from dropdown</li> <li>Navigate to connection source in Instances Tree view on left</li> <li>Right-click on source and select "Set Source"</li> <li>Navigate to connection destination</li> <li>Right-click on destination and select "Add Connection"</li> </ol>	
nstances a sys-0 a node-0 a motherboard-0		2. Select Bus type 3. Select level in hiera	archy
<ul> <li>membuf-0</li> <li>bmc-0</li> <li>vddr_regulator-0</li> <li>vddr_enable-0</li> <li>membuf_vpd-0</li> <li>dimmconn-0</li> <li>dimmconn-1</li> <li>pcieslot-0</li> </ul>		(more about this on ne	ext pa
<ul> <li>▷ membuf-0</li> <li>▷ bmc-0</li> <li>▷ vddr_regulator-0</li> <li>▷ vddr_enable-0</li> <li>▷ membuf_vpd-0</li> <li>▷ dimmconn-0</li> <li>▷ dimmconn-1</li> <li>▷ pcieslot-0</li> </ul>	Field Value	(more about this on ne Description	ext pa
<pre>&gt; membuf-0 &gt; bmc-0 &gt; vddr_regulator-0 &gt; vddr_enable-0 &gt; membuf_vpd-0 &gt; dimmconn-0 &gt; dimmconn-1 &gt; pcieslot-0</pre>	Field Value 1,2,3,9,5,18,1	(more about this on ne Description ADC Channel function id. 16 channels.	ext pa
> membuf-0 > bmc-0 > vddr_regulator-0 > vddr_enable-0 > membuf_vpd-0 > dimmconn-0 > dimmconn-1 > pcieslot-0 Attribute ADC_CHANNEL_FUNC_IDS ADC_CHANNEL_GAINS	Field         Value           1,2,3,9,5,18,1         27397,27397,	(more about this on ne Description ADC Channel function id. 16 channels. ADC channel gain * 1000. 16 channels.	ext pa
membuf-0     bmc-0     vddr_regulator-0     vddr_enable-0     membuf_vpd-0     dimmconn-0     dimmconn-1     pcieslot-0  Attribute ADC_CHANNEL_FUNC_IDS ADC_CHANNEL_GAINS ADC_CHANNEL_GNDS	Field         Value           1,2,3,9,5,18,1         27397,27397,           8,8,8,8,8,8,8,8.8         8,8,8,8,8,8,8.8	Description       Image: Comparison of the second sec	ext pa
membuf-0  bmc-0  vddr_regulator-0  vddr_enable-0  membuf_vpd-0  dimmconn-0  dimmconn-1  pcieslot-0  Attribute  ADC_CHANNEL_FUNC_IDS  ADC_CHANNEL_GAINS  ADC_CHANNEL_OFFSETS	Field         Value           1,2,3,9,5,18,1         27397,27397,           27397,27397,         8,8,8,8,8,8,8           0,0,0,0,0,0,0,0         0,0,0,0,0,0,0	Description       Image: Constraint of the second sec	ext pa
membuf-0  bmc-0  vddr_regulator-0  vddr_enable-0  wddr_vpd-0  dimmconn-0  dimmconn-1  pcieslot-0  Attribute  Attribute ADC_CHANNEL_FUNC_IDS ADC_CHANNEL_GAINS ADC_CHANNEL_GAINS ADC_CHANNEL_OFFSETS ALL_MCS_IN_INTERLEAVING_GROUP	Field         Value           1,2,3,9,5,18,1         27397,27397,           8,8,8,8,8,8,8,8         0,0,0,0,0,0,0	Description         ADC Channel function id. 16 channels.         ADC channel gain * 1000. 16 channels.         ADC Channel ground. 16 channels.         ADC Channel ground. 16 channels.         System attribute.	ext pa

# **Connection Hierarchy**

- Connections must be created at highest common point in hierarchy or physically where wire exists. Here are some examples:
  - Simple single motherboard system
    - All connections are created at motherboard level so Selected Card = motherboard.
    - The motherboard is also obviously where the physical wires exist
  - System with memory riser cards
    - The DMI bus spans the motherboard and riser card
    - The motherboard level is selected because that is the highest common level in the hierarchy
  - Multi-node system with cables connecting nodes
    - System level is selected because that is highest common level in hierarchy

ServerWiz2 - C:\Users	IBM_ADMIN\Documents	GitHub\Server\	Wizard2\ServerWizard	d2\xml\systems\demo.xml	
Add Instances Add B Select Bus: Select Card: Delete Connection	usses 12C motherboard-0 I Show only b	usses of selecte	Connection wil Exist on mothe V to type	Steps for adding a new connection: <b>rboard</b> ect a bus type from dropdown 2. Select the card on which the bus is on from dropdown 3. Navigate to connection source in Instances Tree view on left 4. Right-click on source and select "Set Source" 5. Navigate to connection destination 6. Right-click on destination and select "Add Connection" <b>I2C bus type</b>	t
Instances       bmc-0/bmc_j2c_master => proc_socket-0/module-0/proc/j2c-slave         * node-0       membuf-0/j2c-master => dimmconn-0/dimm-0/spd/j2c-slave         * module-0       membuf-0/j2c-spare => wddr_enable-0/j2c-slave         * proc_socket-0       Motherboard level is automatically highlighted and expanded         * proc       j2c-master-lightpath => i2c-master-lightpath => i2c-master-looplug =>         bmc-0       bmc-0         bmc-0       bmc-0         bmc-0       bmc-0         bmc-0       bmc-0         bmc-0       bmc-0         bmc-0       Source show '=>'					
Attribute	ł	ield	Value	Description	
CLASS			BUS	Attribute indicating the target's class	
I2C_ADDRESS			0xA0	I2C Address	
I2C_SPEED			400	I2C Speed in kHz	
New	Open Save	Save	As Import SI	DR Run Checks	Exit





Select connection to View attributes. For example, An I2C bus has an address and speed.

### Minimum System Requirements



#### DMI

Select Bus:       DMI <ul> <li>Select Card:</li> <li>motherboard-0</li> <li>Delete Connection</li> <li>Instances</li> <li>Sys-0</li> <li>a node-0</li> <li>groc_socket-0</li> <li>a module-0</li> <li>a proc_ mcs-1 (M0 DMI D) =&gt; mcs-4 (M1 DMI D) =&gt; mcs-5 (M1 DMI C) =&gt; mcs-5 (M1 DMI C) =&gt; mcs-6 (M1 DMI D) =&gt; mcs-7 (M2 DMI C) =&gt; mcs-8 (M1 DMI C) =&gt; mcs-8 (M1 DMI C) =&gt; mcs-8 (M1 DMI C) =&gt; mcs-9 (M</li></ul>	Add Instances Add Bu	usses		Steps for adding a new connection:	
Select Card:       motherboard-0 <ul> <li>Right-click on source and select "St Source"</li> <li>Navigate to connection destination</li> <li>Right-click on destination<th>Select Bus:</th><th>DMI</th><th></th><th></th><th>2. Select the card on which the bus is on from dropdown     3. Navigate to connection source in Instances Tree view on left</th></li></ul>	Select Bus:	DMI			2. Select the card on which the bus is on from dropdown     3. Navigate to connection source in Instances Tree view on left
Delete Connection       Instances       6. Right-click on destination         Instances       proc_socket-0/module-0/proc/mcs-1 => membuf-0/dr         * sys-0       modue-0         * proc_socket-0       module-0         * proc_socket-0       mcs-1 (M0 DMI D) => mcs-1 (M0 DMI D) => mcs-5 (M1 DMI C) => mcs-6 (M2 DMI DMI D) => mcs-5 (M1 D	Select Card:	motherboard-0			A. Right-click on source and select "Set Source"     S. Navigate to connection distinction
Instances       proc_socket-0/module-0/proc/mcs-1 => membuf-0/dr         * sys-0       * node-0         * motherboard-0       * motherboard-0         * proc_socket-0       * module-0         * proc_socket-0       * module-0         * module-0       * membuf-0         * mcs-1 (M0 DMI D) =>       mcs-4 (M1 DMI D) =>         mcs-4 (M1 DMI D) =>       mcs-5 (M1 DMI C) =>         * membuf-0       dmi <=         Attribute       Field       Value       Description         CLASS       BUS       Attribute indicating the target's class         DMI_REFCLOCK_SWIZZLE       0       Source: MRW: Downstream MSB Swap and Upstream         PROC_TX_MSBSWAP       0       Source: MRW: Downstream MSB Swap and Upstream	Delete Connection	ly busses of se	lected type	6. Right-click on destination and select "Add Connection"	
# sys-0         # node-0         # motherboard-0         # proc_socket-0         # module-0         # proc         mcs-1 (M0 DMI D) =>         mcs-4 (M1 DMI D) =>         mcs-5 (M1 DMI C) =>         mcs-5 (M1 DMI C) =>         membuf-0         dmi <=         Attribute         Field       Value         Description         CLASS         DM_REFCLOCK_SWIZZLE         MEMBUF_TX_MSBSWAP         PROC_TX_MSBSWAP         O         Source: MRW: Downstream MSB Swap and Upstream         PROC_TX_MSBSWAP         O         Source: MRW: Downstream MSB Swap and Upstream	Instances				proc_socket-0/module-0/proc/mcs-1 => membuf-0/dmi
Attribute     Field     Value     Description       CLASS     BUS     Attribute indicating the target's class       DMI_REFCLOCK_SWIZZLE     BUS     Attribute indicating the target's class       MEMBUF_TX_MSBSWAP     0     Source: MRW: Downstream MSB Swap and Upstream       PROC_TX_MSBSWAP     0     Source: MRW: Downstream MSB Swap and Upstream	▲ motherboar ▲ proc_sou ▲ modu ▲ p ▲ modu ▲ p d mi	rd-0 cket-0 ule-0 roc mcs-0 (M0 DMI D) = mcs-1 (M0 DMI C) = mcs-4 (M1 DMI D) = mcs-5 (M1 DMI C) = -0 <=	> > >		
CLASS       BUS       Attribute indicating the target's class         DMI_REFCLOCK_SWIZZLE       Defines Murano/Venice/Naples FSI GP8 refclock e         MEMBUF_TX_MSBSWAP       0       Source: MRW: Downstream MSB Swap and Upstream         PROC_TX_MSBSWAP       0       Source: MRW: Downstream MSB Swap and Upstream	Attribute		Field	Value	Description
DMI_REFCLOCK_SWIZZLE         Defines Murano/Venice/Naples FSI GP8 refclock e           MEMBUF_TX_MSBSWAP         0         Source: MRW: Downstream MSB Swap and Upstream           PROC_TX_MSBSWAP         0         Source: MRW: Downstream MSB Swap and Upstream	CLASS			BUS	Attribute indicating the target's class
MEMBUF_TX_MSBSWAP         0         Source: MRW: Downstream MSB Swap and Upstream           PROC_TX_MSBSWAP         0         Source: MRW: Downstream MSB Swap and Upstream	DMI_REFCLOCK_SW	ZZLE			Defines Murano/Venice/Naples FSI GP8 refclock enable f
PROC_TX_MSBSWAP 0 Source: MRW: Downstream MSB Swap and Upstream	MEMBUF_TX_MSBSW	AP		0	Source: MRW: Downstream MSB Swap and Upstream MSB
	PROC_TX_MSBSWAP	1		0	Source: MRW: Downstream MSB Swap and Upstream MSB

- Required Connections: All membuf's must have a DMI connection to a CPU
- The DMI name in the parenthesis match the schematic names
- If there is an lane reversal in the design, change the MSBSWAP attributes below to "1".

### DDR

Add Instances Add Busses				Steps for adding a new conr 1. Select a bus type from dro	
Select Bus: DDR3					<ul> <li>2. Select the card on which t</li> <li>3. Navigate to connection st</li> </ul>
Select Card: motherboard-0				4. Right-click on source and     5. Navigate to connection d	
Delete Connection Show on		Show only bus	sses of selected type	6. Right-click on destination	
Instances			×	membuf-0/mba-0/Port A:	CS0 => dimmconn-0/dimm-0/ddr3
sys-0 mode-0 motherboard-0 membuf-0 mba-0 Port A:CS0 => Port A:CS1 => Port B:CS1 => Port B:CS1 => b mba-1 dimmconn-0 a dimm-0		= <b>^</b> ⊨ > > >			

- Required Connections: All DIMMs must be connected to a membuf
- The names for the DDR ports match schematic names. Make sure DIMM naming convention and connections match schematic. It will make the I2C connections more straightforward.

**I**2C

Add Instances Add Busses			Steps for adding a new connection:	
Select Bus: 12C		•	2. Select the card on which the bus is on from dropdown 3. Navigate to connection source in Instances Tree view on left	
Select Card: mother	rboard-0	•	4. Right-click on source and select "Set Source"	
Delete Connection	✓ Show only busses of select	cted type	<ol> <li>Navigate to connection destination</li> <li>Right-click on destination and select "Add Connection"</li> </ol>	
Instances   proc_socket-0  module 0		-	bmc-0/bmc_i2c_master => proc_socket-0/module-0/proc/i2c-slave membuf-0/i2c-master => dimmconn-0/dimm-0/spd/i2c-slave membuf-0/i2c-spare => vddr_enable-0/i2c-slave	
<ul> <li>proc</li> <li>membuf-0</li> <li>i2c-master =:</li> <li>i2c-spare =&gt;</li> <li>bmc-0</li> <li>vddr_enable-0</li> <li>i2c-slave &lt;=</li> <li>membuf_vpd-0</li> <li>i2c-slave &lt;=</li> <li>dimmconn-0</li> <li>dimm-0</li> <li>spd</li> </ul>	>	E .		
Attribute	Field	Value	Description	
CLASS		BUS	Attribute indicating the target's class	
I2C_ADDRESS		0xA0	I2C Address	
I2C_SPEED		400	I2C Speed in kHz	

- Required connections:
  - BMC to CPU I2C slave for OCC communication
  - Membuf I2C connections to DIMMs SPD
  - Membuf I2C connection to GPIO expander to VDDR enable
  - Membuf or CPU I2C connection to VPD
- For I2C busses, make sure I2C\_ADDRESS and I2C\_SPEED attributes match the design

### GPIO

Add Instances Add Busses	Steps for adding a new connection:
Select Bus: GPIO	2. Select the card on which the bus is on from dro     3. Navigate to connection source in Instances Tre
Select Card: motherboard-0	A. Right-click on source and select "Set Source"     S. Navigate to connection destination
Delete Connection Show only busses	selected type 6. Right-click on destination and select "Add Con
Instances	<pre>vddr_enable-0/io-0 =&gt; vddr_regulator-0/vreg_enable</pre>
node-0 motherboard-0	
⊿ vddr_regulator-0	
vreg_pgood =>	E
vddr_enable-0	
io-0 (100) <=>	
io-1 (IO1) <=>	
io-2 (IO2) <=>	
io-3 (IO3) <=>	

• Required Connection: GPIO expander that controls the VDDR regulator enable. The GPIO port # from GPIO expander must match design.

### Power

Add Instances Add B	usses	Steps for adding a new connection:
Select Bus:	POWER	<ul> <li>2. Select the card on which the bus is on from 3. Navigate to connection source in Instance</li> </ul>
Select Card:	motherboard-0	<ul> <li>4. Right-click on source and select "Set Sou 5. Navigate to connection destination</li> </ul>
Delete Connection	Show only busses of selected type	6. Right-click on destination and select "Ad
nstances		vddr_regulator-0/vout => membuf-0/vddr
⊿ sys-0		
node-0 a motherboa	rd-0	
⊿ membu	f-0	
vddr_re	sulator-0	
vou	=>	

• Required Connections: VDDR regulator connection to membuf

#### LPC Bus

Add Instances Add Busses			Steps for adding a new connect 1. Select a bus type from dropdo
Select Bus: LPC			<ul> <li>2. Select the card on which the l</li> <li>3. Navigate to connection source</li> </ul>
Select Card	d: motherboard-0		4. Right-click on source and sele     5. Navigate to connection dectu
Delete Connection Show only		nly busses of selected type	6. Right-click on destination and
Instances		bmc-0/bmc_lpc_master =>	> proc_socket-0/module-0/proc/lpc-slave
0-242			
a bodo 0			
<ul> <li>a node-0</li> <li>a motherbox</li> </ul>	ard-0		
<ul> <li>a node-0</li> <li>a motherbox</li> <li>a proc_s</li> <li>a motherbox</li> </ul>	ard-0 ocket-0		
<ul> <li>a node-0</li> <li>a motherboa</li> <li>a proc_si</li> <li>a motherboa</li> </ul>	ard-0 ocket-0 dule-0 proc		
<ul> <li>a node-0</li> <li>a motherboa</li> <li>a proc_si</li> <li>a motherboa</li> <li>a bmc-0</li> </ul>	ard-0 ocket-0 dule-0 proc Ipc-slave <=		

• Required connection: The LPC bus connection between the BMC and one of the CPU's tells Hostboot which CPU is the master.

### Logical Association

Add Instances Add Bu	usses			Steps for adding a new connection: 1. Select a bus type from dropdown		
Select Bus:	LOGICAL_ASSOC	IATION	<ol> <li>Select the card on which the bus is on from dropdown</li> <li>Navigate to connection source in Instances Tree view on left</li> </ol>			
Select Card: motherboard-0 Delete Connection Show on			1	4. Right-click on source and select "Set Source"     5. Navigate to connection destination		
		w only busses of se	lected type	6. Right-click on destination and select "Add Connection"		
Instances				membuf_vpd-0/vpd_assoc_parent => membuf-0/vpd_assoc_child		
■ sys-0 ■ node-0 ■ motherboar ▷ proc_soo ■ membuf vpd_: ■ membuf vpd_	rd-0 cket-0 -0 assoc_child <= _vpd-0 assoc_parent =>					
Attribute		Field	Value	Description		
CLASS			BUS	Attribute indicating the target's class		
ISDIMM_MBVPD_INDE	EX		0	Multiple centaurs can sometimes have their VPD locate		

- Required connections: VPD that contains membuf wiring information connection to membuf
- This is a virtual connection that tells hostboot where the VPD exists for each membuf. The VPD can alternatively be attached to the CPU I2C master
- This logical association concept could be extended to FRU LED associations

### PCle

Add Instances	Add Bu	isses			
Select Bus:		PCIE			
Select	Card:	motherboard-0			
Delete Conne	ction		Show or	nly busses of selected type	
nstances					
⊿ node-0 ⊿ mother	erboar	d-0			
	oc_soc modu	ile-0			
	⊿ pr	oc			
	4	pci_conf	igs		
		⊳_E0_x	16:E1_x16		
		⊳ <u>E0_</u> x	16:E1_	Deconfig	
Attribute				Select Config	
BUS TYPE					NA

 The PCIe bus is unique in that it can be configured in several different ways. Under the "pci\_configs" parent, you will see the various configurations. To choose one, right-click and select "Select Config". The other configurations will be hidden. To make all configurations visible, right-click and select "Deconfig".

### PCle



- Required Connections: None
- Here is an example where E0 is configured as a x16 and E1 is configured as 2 x8's.

#### Serverwiz Library Files

- Every 3 days, Serverwiz will check the XML files at:
  - <u>https://github.com/open-power/serverwiz/tree/master/xml</u>
  - If the files have change, it will prompt the user if they wish to download new files
- Description of Files:
  - attribute\_types.xml, attribute\_types\_hb.xml, attribute\_types\_mrw.xml
    - Listing of all possible attributes including data type, default value, and description
    - Attribute\_type.xml and attribute\_types\_hb.xml are same as hostboot
  - target\_types\_mrw.xml
    - Describes all the possible instances, the type of the instance, and the associated attributes
    - Systems, chips, cards, etc
    - Also describes the valid parent instance types
  - target\_instances\_v3.xml
    - Specific instances of a target types including attribute values and children

## Making Changes

- Additions of attributes might occur because of new Hostboot requirements
   or features
- The change must first be thought of from a end user point of view (system designer/user of Serverwiz)
  - If the attribute is a simple value that the user can enter directly then the developer must simply add the attribute into Serverwiz's attribute\_types.xml and target\_types\_mrw.xml under appropriate target. The attribute will then show up in the Serverwiz and the user can enter desired value.
  - 2. If the attribute is a computed value based on several factors, then the handling of the attribute must be in processMrw.pl
    - If the computed valued depends on static values that are also new attributes, then follow Step 1.

### **IPMI** Sensor Overview

- The POWER BIOS (Hostboot, OCC, and OPAL) updates virtual BMC sensors via IPMI set sensor commands. The BIOS has to be informed of the IPMI sensor ID's. It does so through IPMI\_SENSORS attribute.
- Serverwiz can import XML (which could be generated by BMC development environment) that contains the sensor information using the "Import SDR" button and populate the IPMI\_SENSORS attribute
- There is a strict mapping between the instance type and the IPMI entity ID. The mapping is maintained in the ENTITY\_ID\_LOOKUP attribute which is read-only (see Table on next page)
- The Serverwiz IPMI\_INSTANCE attribute maps to the IPMI entity instance in the SDR import XML. The user must make sure the IPMI\_INSTANCE attribute is unique per entity ID.

# **Entity ID Mapping**

Sensor Name	Entity ID	Sensor Type Code	Target Association
CPU Temperature	0x03	0x01	proc
CPU Core Temperature	0xD0	0x01	core
CPU Functional Status	0x03	0x07	proc
CPU Core Functional Status	0xD0	0x07	core
CPU Core Frequency	0xD0	0xC1	core
Memory Buffer Temperature	0xD1	0x01	membuf
DIMM Temperatur	0x20	0x01	dimm
Memory Buffer Functional	0xD1	0x0C	membuf
DIMM Functional Status	0x20	0x0C	dimm
DIMM Frequency	0x08	0xC1	system
Backplane Fault	0x07	0xC7	node
System Event	0x01	0x12	system
Host Status	0x23	0x22	system
Firmware Boot Progress	0x22	0x0F	system
PCIe Link Present	0x23	0xC4	system
OS Boot Status	0x23	0x1F	system
OCC Active	0xD2	0x07	occ
Power Cap	0x17	0xC2	system
Boot Count	0x22	0xC3	system
System Power Limit (User set)	0x15	0xC2	system
Activate Power Limit	0x15	0xC6	system
APSS_Channel	0xD7	0xC2	system

\* Entity Instance from SDR maps to IPMI\_INSTANCE attribute

#### SDR XML Format

When the "Import SDR" button is used in Serverwiz, it will import a file of this format. It will attempt to match up the entity id from the ENTITY\_ID\_LOOKUP under a given target and the IPMI\_INSTANCE attribute to the entity\_id and entity\_instance fields from the SDR import file.